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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/586,775	06/05/2000	Gil Vinitzky	P-2596-US	7937

27130 7590 08/04/2004	EXAMINER	
EITAN, PEARL, LATZER & COHEN ZEDEK LLP	DO, CHAT C	
10 ROCKEFELLER PLAZA, SUITE 1001		
NEW YORK, NY 10020	ART UNIT	PAPER NUMBER
	2124	

DATE MAILED: 08/04/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)
Advisory Action	09/586,775	VINITZKY, GIL
	Examiner	Art Unit	
	Chat C. Do	2124	

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 14 June 2004 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may <u>only</u> be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

<div align="center">PERIOD FOR REPLY [check either a) or b)]</div>

a) ☐ The period for reply expires _____ months from the mailing date of the final rejection.

b) ☒ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection. ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. ☐ A Notice of Appeal was filed on _____. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.

2. ☐ The proposed amendment(s) will not be entered because:

    (a) ☐ they raise new issues that would require further consideration and/or search (see NOTE below);

    (b) ☐ they raise the issue of new matter (see Note below);

    (c) ☐ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or

    (d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.

    NOTE: _____.

3. ☒ Applicant's reply has overcome the following rejection(s): <u>See below.</u>

4. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).

5. ☒ The a) ☐ affidavit, b) ☐ exhibit, or c) ☒ request for reconsideration has been considered but does NOT place the application in condition for allowance because: *See below.*

6. ☐ The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.

7. ☒ For purposes of Appeal, the proposed amendment(s) a) ☐ will not be entered or b) ☒ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

    The status of the claim(s) is (or will be) as follows:

    Claim(s) allowed: _____.

    Claim(s) objected to: _____.

    Claim(s) rejected: *1-6*.

    Claim(s) withdrawn from consideration: _____.

8. ☐ The drawing correction filed on _____ is a) ☐ approved or b) ☐ disapproved by the Examiner.

9. ☐ Note the attached Information Disclosure Statement(s)(PTO-1449) Paper No(s). _____.

10. ☐ Other: _____.

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Part 3: the applicant's reply has overcome the rejection of claims 7-8 by cancelling claims 7-8.

Part 5(c): The applicant argues in response that the cited reference by Avellar et al. disclose six memory banks: 10, 12, 16, 18, 20, and 22 instead of two memory banks as claimed by examiner. However as seen clearly in the Figure 1 of cited reference, the first pass has only two memory banks and wherein the subsequence pass(es) utilize those two memory banks. Therefore in rejection of claims 1-6, Hancke et al. disclose in Figures 1 and 9 a method for in-place (col. 1 lines 35-40) memory management in a DSP architecture performing a Fast Fourier Transformation upon a sequence of N data points (N = 32 in Figure 9) and the sequence numbered from 0 to N-1. The method comprises storing each of data points (after first pass) numbered from 0 to (N/2)-1 in a first memory space X (24) and each of data points numbered N/2 to N-1 in a second memory space Y (25); for each FFT stage 0 (Pass 2 in Figure 9) data point grouping (0&16, 1&17, 2&18,...,15&31) comprising a first data point of data points in first memory space X (0, 1, 2,...,15) and a corresponding second data point of data points in second memory space Y (16, 17, 18, 31); determining the parity of a data point memory index (col. 5 lines 10-18) corresponding to first and second data points; storing (Table II in col. 4), if parity is of a first parity value (Memory Location is even), the results of an FFT operation upon first data point at the memory address in first memory space X front which first data point was fetched and the result of an FFT operation upon second data point at the memory address in second memory space Y from which second data point was fetched (PO0 in 24 & PO1 in 25; OP6 in 24 & OP7 in 25 ...); and storing (Table II in col. 4), if parity is of a second parity value (Memory Location is odd), the results of an FFT operation upon first data point at the memory address in second memory space Y from which second data point was fetched and the result of an FFT operation upon second data point at the memory address in first memory space X from which first data point was fetched (data results are swapped while storing such as OP3 in 24 & OP4 in 25, OP5 in 24 & OP6 in 25...). Hancke et al. do not disclose storing each of raw data points in a first memory space X and second memory space Y. The main different between the reference and the present application is the initial stage wherein all the input data points are stored in the first and second memory storage prior the first stage FFT operation in the present application and the input data points are stored temporarily in a third memory storage, passed through first stage of FFT operation, than stored in the first and second memory storage in the reference. However, storing all the input data points in the first and second memory space is conventional method in computing FFT as seen in Figure 1 of Avellar et al.'s invention. Avellar et al. disclose in Figure 1 an operation of FFT wherein the input data points are stored in the first and second memory space prior starting the first stage of FFT operation. Therefore, it would have been obvious to a person having ordinary skill in the art at the time the invention is made to modify the Hancke et al.'s invention Figure 1 as seen in Avellar et al.'s invention by storing all the input data points directly into the first and second memory spaces (24 and 25) because it would enable to increase the system performance by simultaneously processing the input data points.

JOHN CHAVIS
PATENT EXAMINER
ART UNIT 2124